



ATTORNEY DOCKET NO. 114596-28-000053BS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No.: 09/626,325 Confirmation No.: 7939
Applicant: John S. Yates, Jr., et al.
Title: OPERATING SYSTEM FOR COMPUTER WITH TWO ARCHITECTURES
Filed: July 26, 2000
Art Unit: 2183
Examiner: Richard Ellis
Atty. Docket: 114596-28-000053BS
Customer No. 38492

AMENDMENT ACCOMPANYING RCE

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Applicant hereby responds to the Office Action of October 27, 2004. Kindly amend the application as follows.

PETITION FOR EXTENSION OF TIME

Time to respond to the Office Action of October 27, 2004 has previously been extended to April 27, 2005. In the event further extension is required, Applicant petitions for such extension as may be necessary. Kindly charge the petition fee to Deposit Account No. 23-2405, Order No. 114596-28-000053BS.

AMENDMENTS TO THE SPECIFICATION begin on page 2 of this paper.

AMENDMENTS TO THE CLAIMS begin on page 3 of this paper.

REMARKS/ARGUMENTS begin on page 14 of this paper.

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I certify that this correspondence, along with any documents referred to therein, is being deposited with the United States Postal Service on April 27, 2005 as First Class Mail in an envelope with sufficient postage addressed to Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

[Signature]

Amendment Accompanying RCE
This paper dated April 27, 2005

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If the Examiner believes himself exempt from the Director's instructions in MPEP § 2111.01, Applicant requests a written statement from some Patent Office official who has authority to create such an exemption. Without such a written statement, Applicant respectfully requests that the Examiner observe those instructions.

If the Examiner believes that his interpretation of "thread" is "reasonable" and consistent with the understanding of those of ordinary skill, Applicant requests a reference showing actual usage of the word "thread" to include "everything from a single instruction, to a conventional subroutine, to the purely hardware 'process' of changing a transistor state from 0 to 1, to loading the operating system into memory on cold boot before the operating system's thread mechanisms are even initialized, to any mechanism for handling an interrupt" as discussed at Applicant's paper of March 30, 2005, at page 7.

V. Conclusion

In view of the amendments and remarks, Applicant respectfully submits that the claims are in condition for allowance, and requests that the application be passed to issue in due course. The Examiner is urged to telephone Applicant's undersigned counsel at the number noted below if it will advance the prosecution of this application, or with any suggestion to resolve any condition that would impede allowance. Kindly charge any additional fee, or credit any surplus, to Deposit Account No. 23-2405, Order No. 114596-28-000053BS.

Respectfully submitted,

WILLKIE FARR & GALLAGHER LLP

Dated: April 27, 2005

By: 

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